

CLAIMS

1. Differential input stage (1) of electronic equipment, comprising means for reducing the interference caused by a voltage or current applied in common mode to two inputs (A; B) of this stage; this stage comprising two channels each connecting one input (A; B) to one output (a; b); each channel comprising first means for reducing interference, these first means comprising, on each of the channels, means (S3; S4) for adding to the input voltage of the channel concerned a first feedback voltage (V1), and means (R1, R2, I3) for supplying a first feedback voltage that is equal to half the sum of the voltages (VA; VB) present at the inputs (A; B), respectively, with the opposite sign; characterized in that, to reduce the effect of a delay introduced by the components of the means (R1, R2, I3) for supplying the first feedback voltage (V1), this stage further comprises means (S5; S6) for further adding to the input voltage of each channel a second feedback voltage (Va1; Vb1) and means (R1, R2; I1, S1; I2, S2) for supplying a second feedback voltage (Va1; Vb1) that is a function of the voltage (VA; VB) at the input corresponding to this channel, with the opposite sign, and with a delay identical to that caused by the components of the means (R1, R2, I3) for supplying the first feedback voltage (V1).
2. Stage according to Claim 1, characterized in that it further comprises, on the upstream side of the first means for reducing interference, second means for reducing common mode interference, comprising, on each of the channels, means (S3'; S4') for adding to the input voltage of the channel concerned a third feedback voltage (V3), and means (R1', R2', I3') for supplying a third feedback voltage that is equal to k times half the sum of the voltages (VA'; VB') present at the inputs (A'; B'), respectively, of the differential stage, with the opposite sign; k being a positive integer less than or equal to 1.
3. Stage according to Claim 2, characterized in that the means (R1', R2', I3') for supplying a third feedback voltage (V3) comprise a bridge of two resistors (R1', R2'), connected to the inputs (A', B') of the differential stage, and an inverter (I3') having an input connected to the mid-point of the bridge and having an output supplying the third feedback voltage (V3).